

Course Schedule - Fall 2005

Electrical and Computer Engineering

420 **Digital Signal Processing Lab** Credit: 2 hours.

(ECE 320) Development of real-time digital signal processing (DSP) systems using a DSP microprocessor; several structured laboratory exercises, such as sampling and digital filtering, followed by an extensive DSP project of the student's choice. Prerequisite: ECE 410.

CRN	Type	Section	Time	Days	Location	Instructor
31821	laboratory	AB1	02:00 PM - 03:50 PM	T	room 251 Everitt Elec and Comp Engr Lab	Yin, Z; Tidemann, J
31826	laboratory	AB2	02:00 PM - 03:50 PM	R	room 251 Everitt Elec and Comp Engr Lab	Yin, Z; Tidemann, J
31829	laboratory	AB3	02:00 PM - 03:50 PM	W	room 251 Everitt Elec and Comp Engr Lab	Kleffner, M; Ko, G
31833	laboratory	AB4	03:00 PM - 04:50 PM	F	room 251 Everitt Elec and Comp Engr Lab	Yin, Z; Ko, G
31840	lecture	AL1	02:00 PM - 02:50 PM	M	room 165 Everitt Elec and Comp Engr Lab	Jones, D