

Course Schedule - Fall 2005

Electrical and Computer Engineering

444 *IC Device Theory & Fabrication* Credit: 4 hours.

(ECE 344) Laboratory and lecture course on the physical theory, design, and fabrication of devices suitable for integrated circuitry; includes the electrical properties of semiconductors and techniques (epitaxial growth, oxidation, photolithography diffusion, ion implantation, metallization, characterization) for fabricating integrated circuit devices such as p-n junction diodes, bipolar transistors, and field effect transistors. Prerequisite: ECE 440.

CRN	Type	Section	Time	Days	Location	Instructor
36808	laboratory	AB1	02:00 PM - 04:50 PM	F	room 50M Everitt Elec and Comp Engr Lab	Sievers, D; Pawlowski, D
36809	laboratory	AB2	08:00 AM - 10:50 AM	T	room 50M Everitt Elec and Comp Engr Lab	Sievers, D; Reddy, U
36819	laboratory	AB3	12:00 PM - 02:50 PM	T	room 50M Everitt Elec and Comp Engr Lab	Sievers, D; Verma, V
36817	laboratory	AB4	02:00 PM - 04:50 PM	W	room 50M Everitt Elec and Comp Engr Lab	Chen, G; Sievers, D
36816	laboratory	AB5	09:00 AM - 11:50 AM	R	room 50M Everitt Elec and Comp Engr Lab	Sievers, D; Pawlowski, D
36814	laboratory	AB6	02:00 PM - 04:50 PM	M	room 50M Everitt Elec and Comp Engr Lab	Sievers, D; Chu, H
41682	laboratory	AB7	02:00 PM - 04:50 PM	R	room 50M Everitt Elec and Comp Engr Lab	Sievers, D; Reddy, U
36813	discussion-recitation	AD1	09:00 AM - 09:50 AM	MWF	room 245 Everitt Elec and Comp Engr Lab	Coleman, J
36811	discussion-recitation	AD2	10:00 AM - 10:50 AM	MWF	room 245 Everitt Elec and Comp Engr Lab	Lyding, J