

# Course Schedule - Spring 2006

## Electrical and Computer Engineering

420 **Digital Signal Processing Lab** Credit: 2 hours.

Development of real-time digital signal processing (DSP) systems using a DSP microprocessor; several structured laboratory exercises, such as sampling and digital filtering, followed by an extensive DSP project of the student's choice. Prerequisite: ECE 410.

CRN	Type	Section	Time	Days	Location	Instructor
33739	laboratory	ABA	02:00 PM - 03:50 PM	T	room 251 Everitt Elec and Comp Engr Lab	Hasegawa-Johnson, M; Guan, K; Tidemann, J
33740	laboratory	ABB	02:00 PM - 03:50 PM	R	room 251 Everitt Elec and Comp Engr Lab	Yin, Z; Kleffner, M; Hasegawa-Johnson, M
33741	laboratory	ABC	02:00 PM - 03:50 PM	W	room 251 Everitt Elec and Comp Engr Lab	Kleffner, M; Hasegawa-Johnson, M; Tidemann, J
33742	laboratory	ABD	03:00 PM - 04:50 PM	F	room 251 Everitt Elec and Comp Engr Lab	Yin, Z; Hasegawa-Johnson, M; Tidemann, J
33743	laboratory	ABE	03:00 PM - 04:50 PM	M	room 251 Everitt Elec and Comp Engr Lab	Hasegawa-Johnson, M; Guan, K; Tidemann, J
33744	lecture	AL1	02:00 PM - 02:50 PM	M	room 165 Everitt Elec and Comp Engr Lab	Hasegawa-Johnson, M