Course Schedule - Spring 2006

Electrical and Computer Engineering

425 ***Intro VLSI System Design*** Credit: 3 hours.
Complementary Metal-Oxide Semiconductor (CMOS) technology and theory; CMOS circuit and logic design; layout rules and techniques; circuit characterization and performance estimation; CMOS subsystem design; Very-Large-Scale Integrated (VLSI) systems design methods; VLSI Computer Aided Design (CAD) tools; laboratory experience in custom VLSI chip design on workstations using concepts of cell hierarchy; final project involving specification, design, and evaluation of a VLSI chip or VLSI CAD program; and written report and oral presentation on the final project. Same as CS 435 and CSE 433. Prerequisite: ECE 385 and ECE 411; or CS 232.

CRN	Type	Section	Time	Days	Location	Instructor
33850	laboratory	AB1	ARRANGED			Chen, D
33853	lecture	AL1	11:00 AM - 12:20 PM	MW	room 269 Everitt Elec and Comp Engr Lab	Chen, D