Course Schedule - Spring 2006

Electrical and Computer Engineering

444 ***IC Device Theory & Fabrication*** Credit: 4 hours.
Laboratory and lecture course on the physical theory, design, and fabrication of devices suitable for integrated circuitry; includes the electrical properties of semiconductors and techniques (epitaxial growth, oxidation, photolithography diffusion, ion implantation, metallization, and characterization) for fabricating integrated circuit devices such as p-n junction diodes, bipolar transistors, and field effect transistors. Prerequisite: ECE 440.

CRN	Type	Section	Time	Days	Location	Instructor
33902	laboratory	AB1	09:00 AM - 11:50 AM	T	room 50M Everitt Elec and Comp Engr Lab	Kondratko, P; Sievers, D
33903	laboratory	AB2	02:00 PM - 04:50 PM	T	room 50M Everitt Elec and Comp Engr Lab	Sievers, D; Hosmane, S
33904	laboratory	AB3	09:00 AM - 11:50 AM	R	room 50M Everitt Elec and Comp Engr Lab	Kondratko, P; Sievers, D
33905	laboratory	AB4	02:00 PM - 04:50 PM	R	room 50M Everitt Elec and Comp Engr Lab	Sievers, D; Chu, H
33906	laboratory	AB5	02:00 PM - 04:50 PM	M	room 50M Everitt Elec and Comp Engr Lab	Sievers, D; Wayne, M
33907	laboratory	AB6	02:00 PM - 04:50 PM	W	room 50M Everitt Elec and Comp Engr Lab	Sievers, D; Reddy, U
33908	laboratory	AB7	02:00 PM - 04:50 PM	F	room 50M Everitt Elec and Comp Engr Lab	Sievers, D; Hosmane, S
33900	discussion-recitation	AD1	09:00 AM - 09:50 AM	MWF	room 245 Everitt Elec and Comp Engr Lab	Coleman, J
33901	discussion-recitation	AD2	10:00 AM - 10:50 AM	MWF	room 245 Everitt Elec and Comp Engr Lab	Choquette, K