

Course Schedule - Fall 2008

Computer Science

431 **Embedded Sys Arch and Software** credit: 0 to 4 hours.

A survey of sampled data systems and embedded architecture; key concepts in common embedded system applications; signal processing and control; embedded microprocessor and device interface; time-critical I/O handling; data communications; real-time operating systems and techniques for the development and analysis of embedded real-time software; hands-on laboratory projects. 3 undergraduate hours. 3 or 4 graduate hours.

Prerequisite: CS 241 or ECE 391.

Students must register for one lab and one lecture section.

CRN	Type	Section	Time	Days	Location	Instructor
40100	laboratory	AB1	03:00 PM - 04:50 PM	W	room ARR Digital Computer Laboratory	Sha, L; Young, J
40101	laboratory	AB2	05:00 PM - 06:50 PM	W	room ARR Digital Computer Laboratory	Sha, L; AlMalkawi, M
31526	laboratory	AB3	05:00 PM - 06:50 PM	R	room ARR Digital Computer Laboratory	Sha, L; AlMalkawi, M
40102	laboratory	AB4	02:00 PM - 03:50 PM	F	room ARR Digital Computer Laboratory	Sha, L; Young, J
40667	lecture-discussion	AE3	11:00 AM - 12:15 PM	TR	room 112 Transportation Bldg	Sha, L
40667: 3 hours						
40668	lecture-discussion	AE4	11:00 AM - 12:15 PM	TR	room 112 Transportation Bldg	Sha, L
40668: 4 hours						